

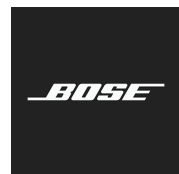
Revision		Comments
A	2016/03/12	1st revision
A1	2016/03/24	<p>A1(P16)Add comment TDM setting.</p> <p>A1(P16)These pins are used for the case of Redline SLOTADDR0TA/B SLOTADDR1TA/B If the Redline debug connector is not used, the address will fixed to defined value.</p> <p>A1(P14,P15,P16)DACRESETn and DACSMUTE function is separated 2 DACs because 2nd I2C port is added.</p> <p>A1(P16)2nd I2C expander is added.</p> <p>A1(P18)TDM_Dir0 & TDM_Dir1 to 10K</p> <p>A1(P18)Correct FPC connector pin3 to MODDETECT3 for definition.</p> <p>A1(P18)Separate Redline SLOTADDR0T and SLOTADDR1T to A/B signal. Remove jumpers from this line.</p> <p>A1(P18)Connect all I2C DSA/SCL to one bus.</p> <p>A1(P10,11,12,13)Channel and polarity swap is removed.</p> <p>A1(P14,P15)Logic Buffer and jumpers removed.</p> <p>A1(P16,18)Pull up for MODINTn is moved to P18.</p>
	2016/03/26	A1(P18) Add 2 of 4.7ohm resistor for thermal spreading.

Schematic contents

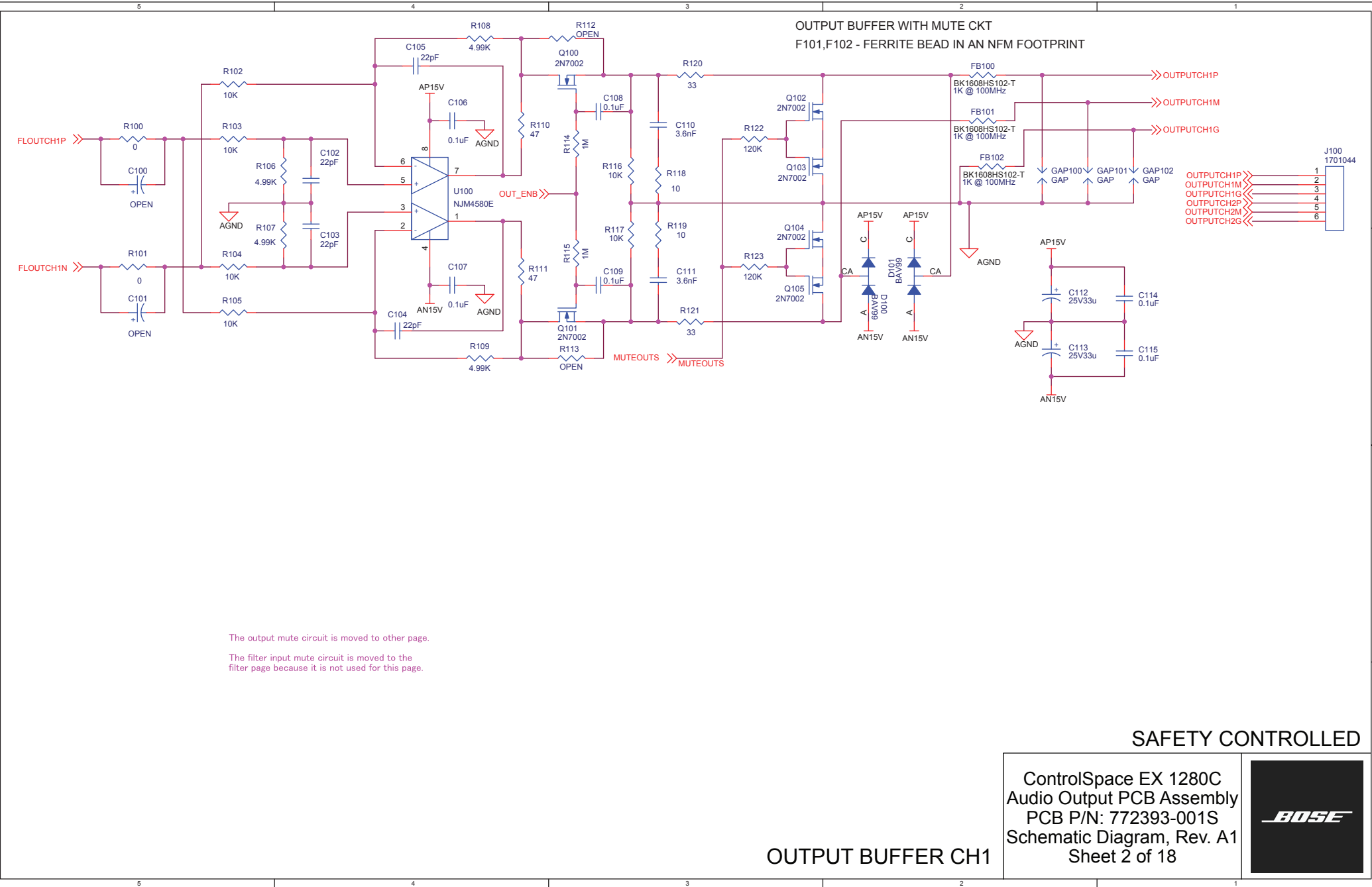
P02(100)OutputBufferCH1
 P03(150)OutputBufferCH2
 P04(200)OutputBufferCH3
 P05(250)OutputBufferCH4
 P06(300)OutputBufferCH5
 P07(350)OutputBufferCH6
 P08(400)OutputBufferCH7
 P09(450)OutputBufferCH8
 P10(500)FILTER – CH12
 P11(550)FILTER – CH34
 P12(600)FILTER – CH56
 P13(650)FILTER – CH78
 P14(700)DAC–1234
 P15(750)DAC–5678
 P16(800)I2C – MUTE
 P17(850)POWER OFF MUTE
 P18(900)POWER, Connectors

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COVER PAGE



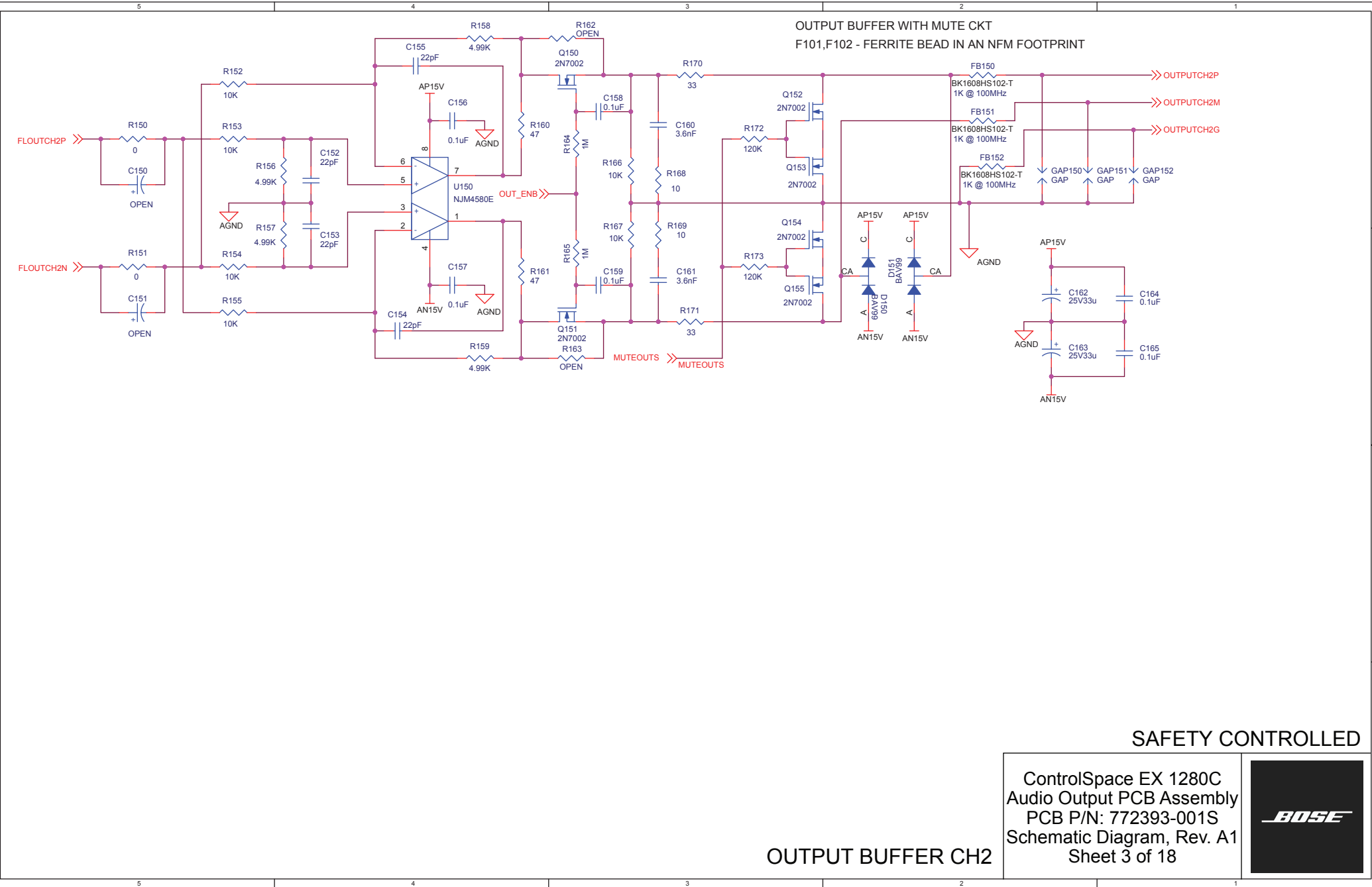
The output mute circuit is moved to other page.
 The filter input mute circuit is moved to the filter page because it is not used for this page.

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OUTPUT BUFFER CH1



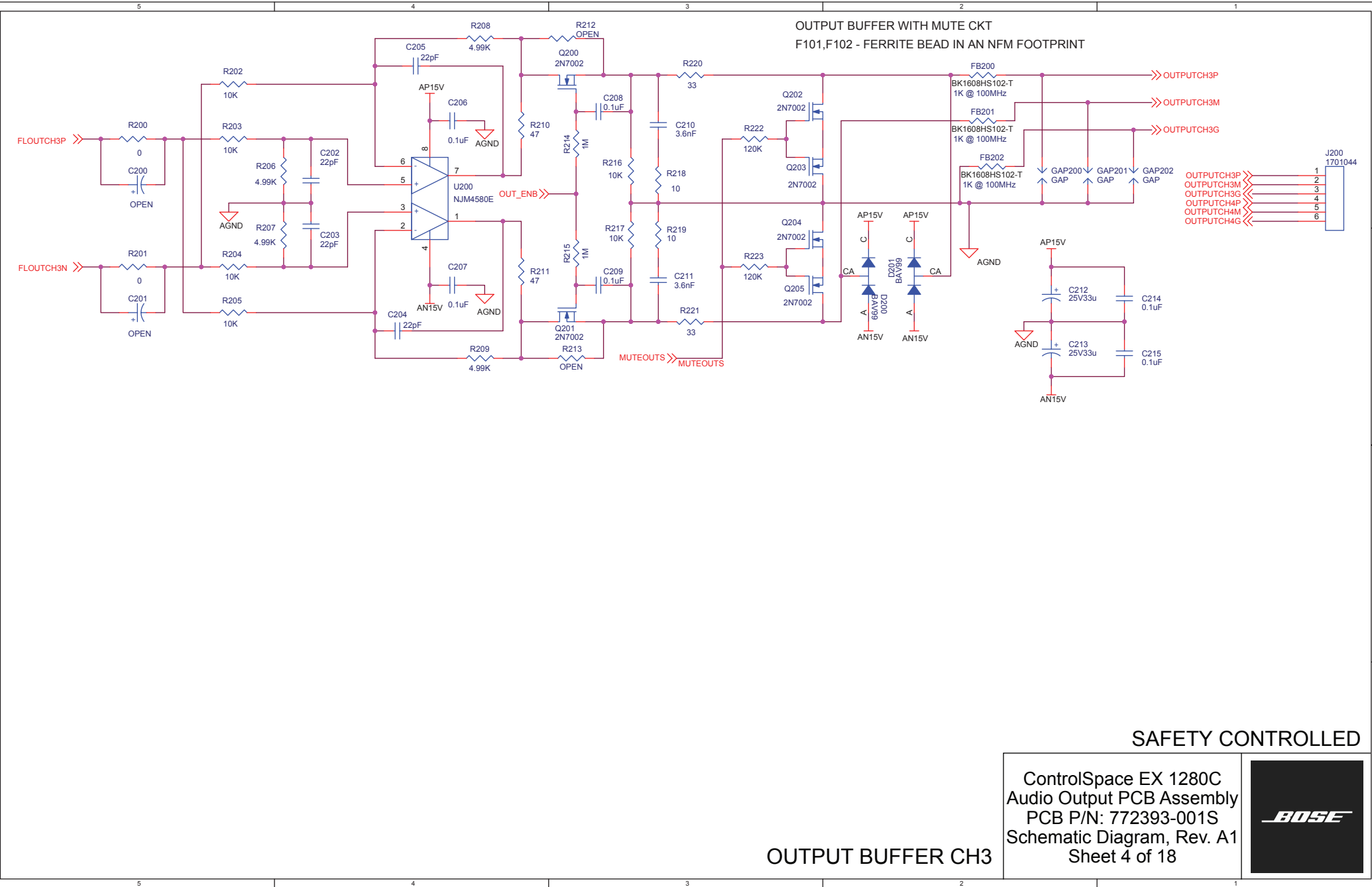
OUTPUT BUFFER WITH MUTE CKT
 F101,F102 - FERRITE BEAD IN AN NFM FOOTPRINT

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OUTPUT BUFFER CH2



OUTPUT BUFFER WITH MUTE CKT
 F101,F102 - FERRITE BEAD IN AN NFM FOOTPRINT

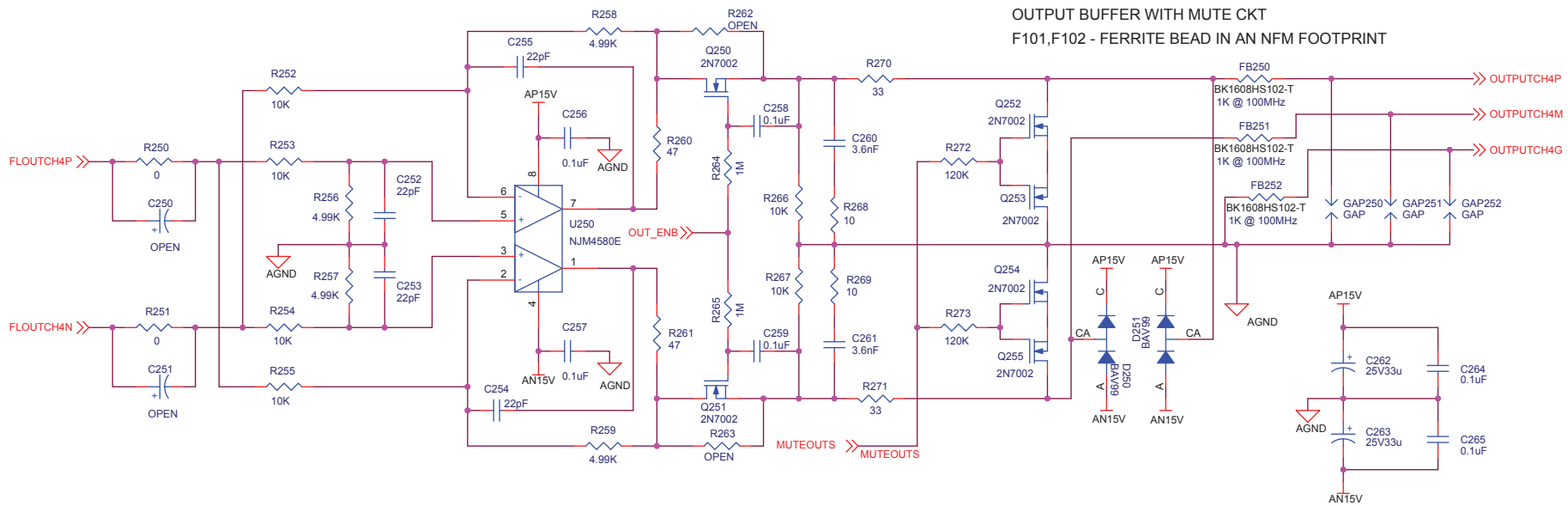
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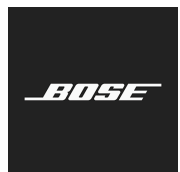
OUTPUT BUFFER CH3

OUTPUT BUFFER WITH MUTE CKT
 F101,F102 - FERRITE BEAD IN AN NFM FOOTPRINT

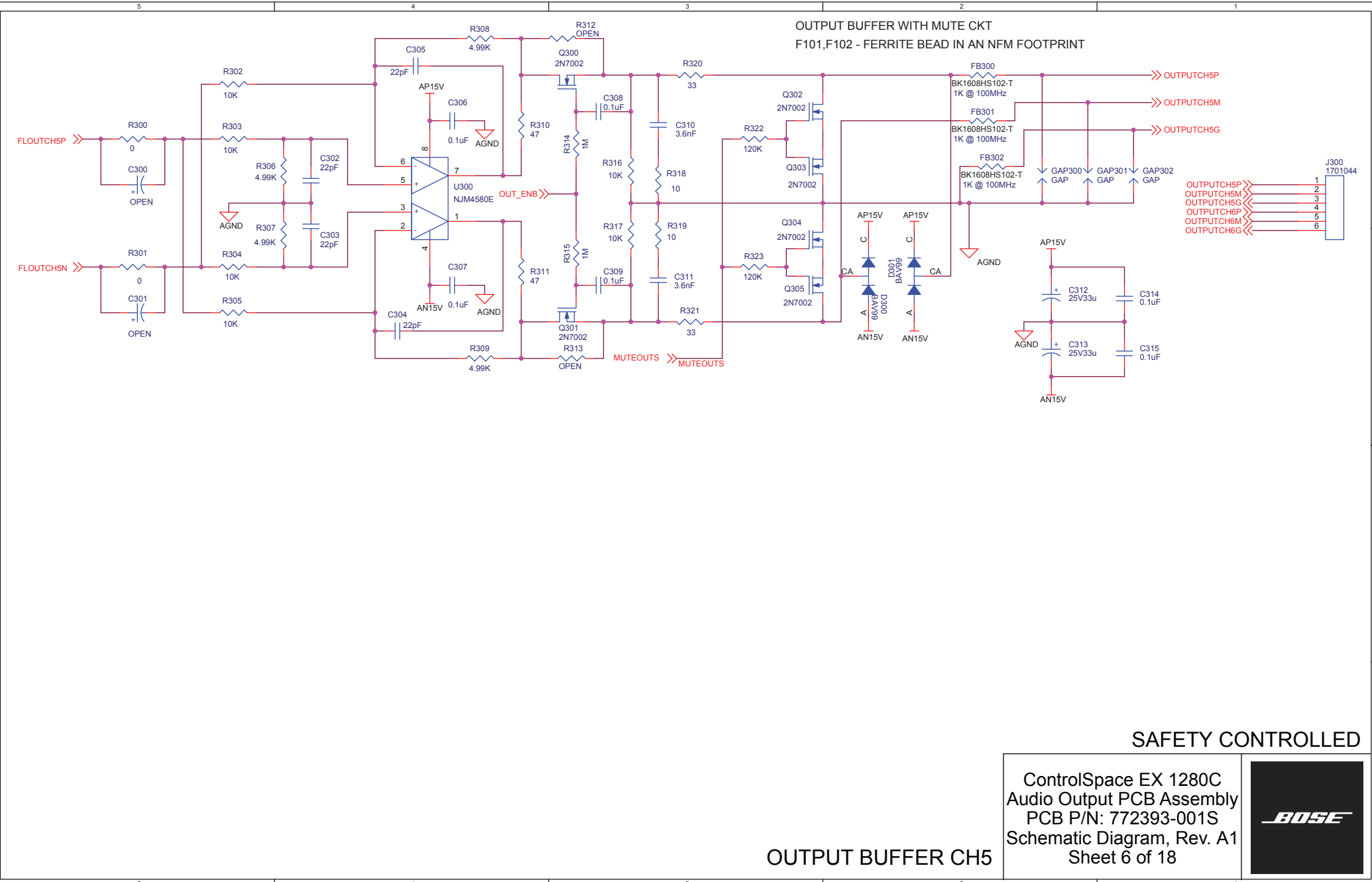


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OUTPUT BUFFER CH4



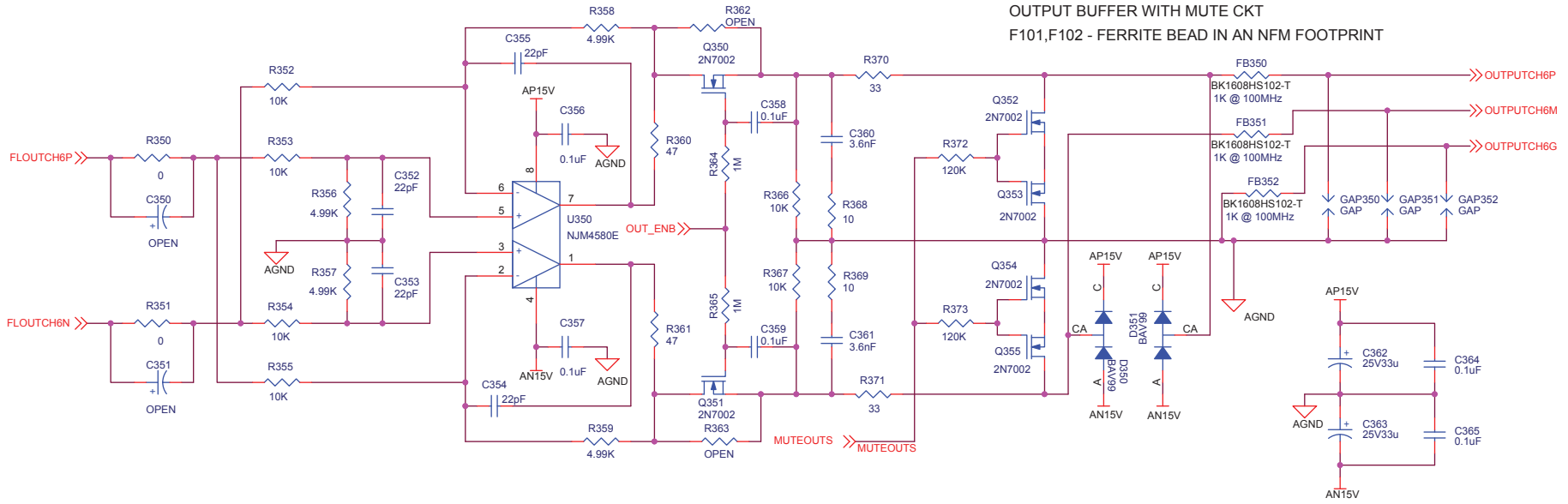
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OUTPUT BUFFER CH5

OUTPUT BUFFER WITH MUTE CKT
 F101,F102 - FERRITE BEAD IN AN NFM FOOTPRINT

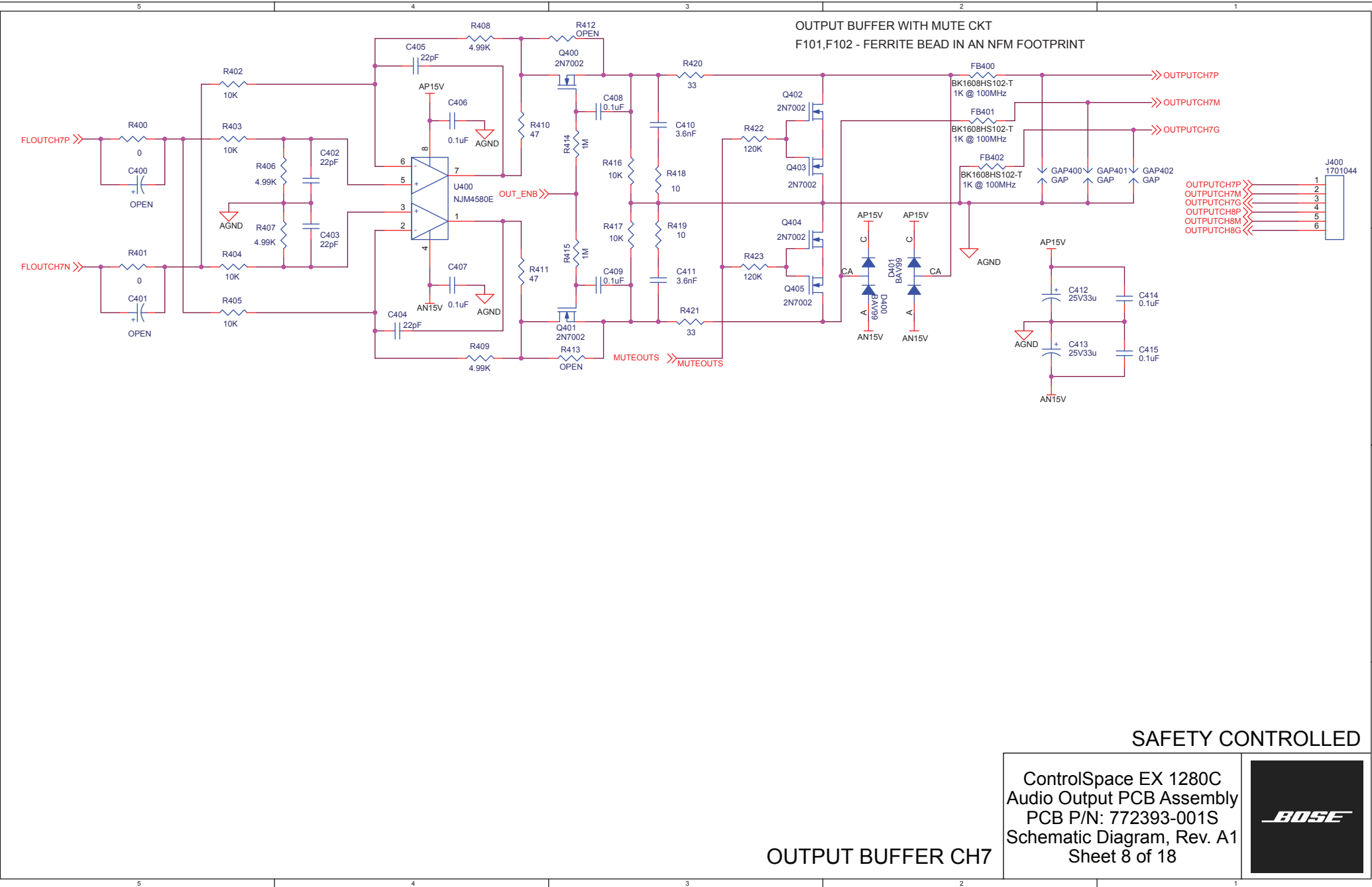


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OUTPUT BUFFER CH6



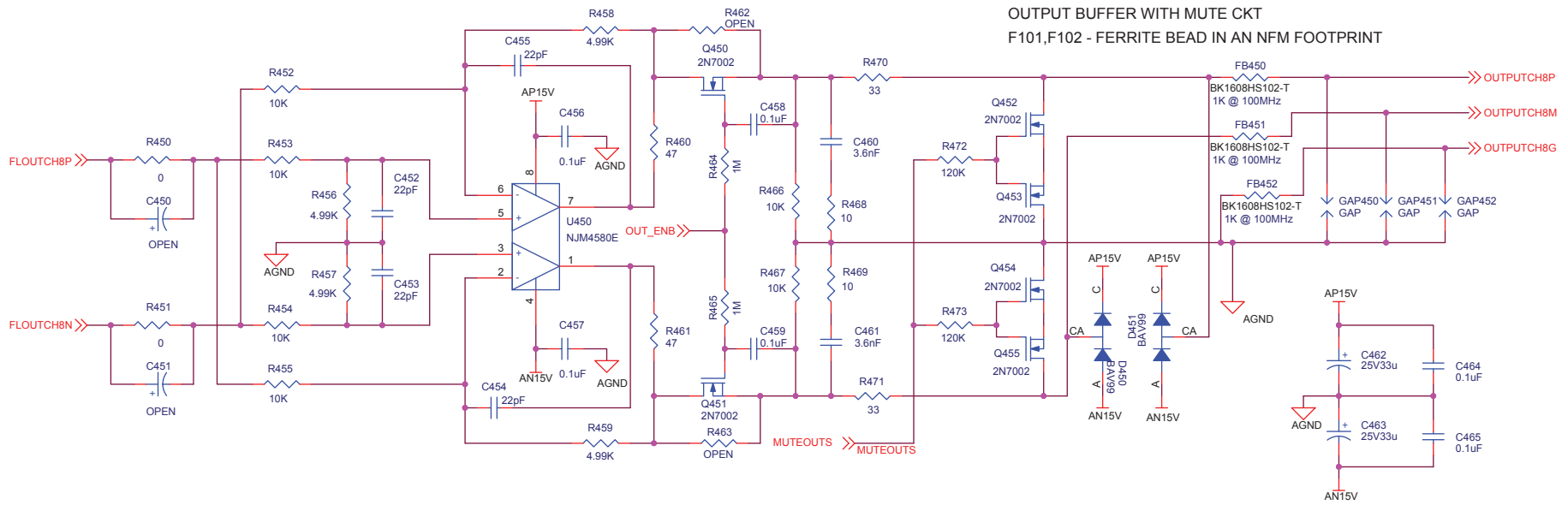
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OUTPUT BUFFER CH7

OUTPUT BUFFER WITH MUTE CKT
 F101,F102 - FERRITE BEAD IN AN NFM FOOTPRINT

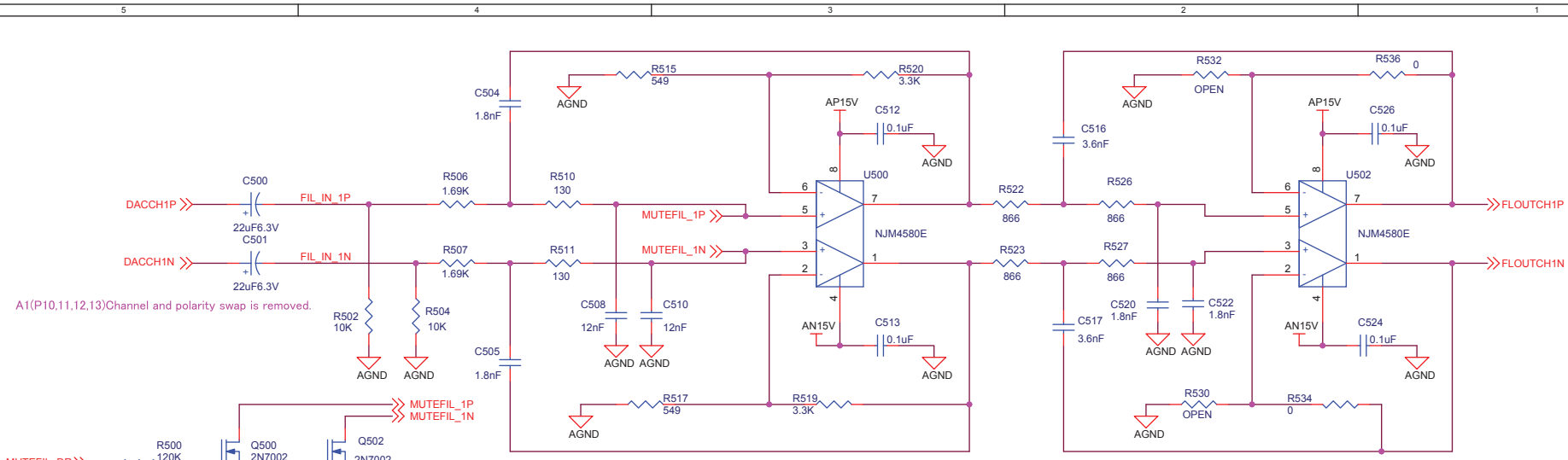


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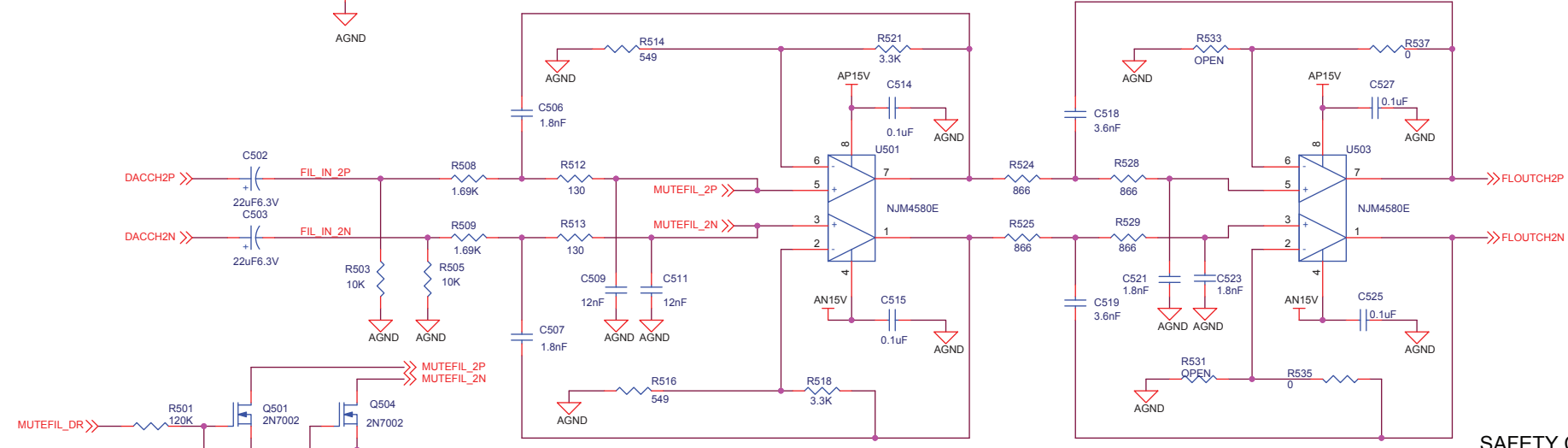
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OUTPUT BUFFER CH8



4TH ORDER BUTTERWORTH SALLEN-KEY MFB HYBRID LOWPASS FILTER
 FIRST STAGE GAIN = 16; SECOND GAIN = 1
 CUTOFF STARTS AT 40KHz, 3dB AT 50KHz

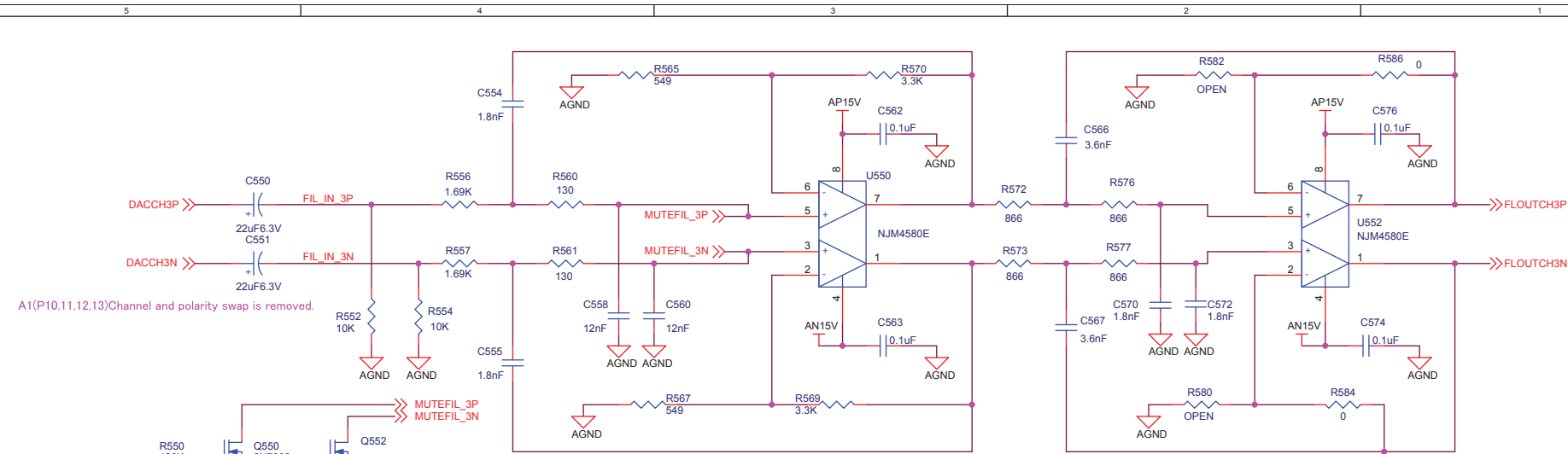


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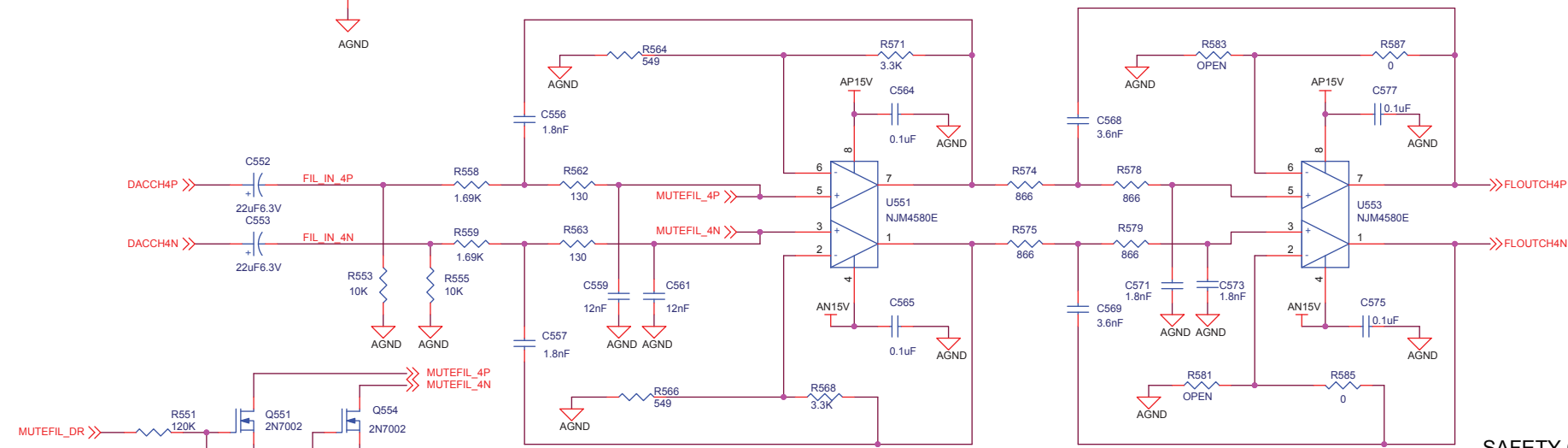
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OUTPUT FILTERS CH1-2



4TH ORDER BUTTERWORTH SALLEN-KEY MFB HYBRID LOWPASS FILTER
 FIRST STAGE GAIN = 16; SECOND GAIN = 1
 CUTOFF STARTS AT 40KHz, 3dB AT 50KHz

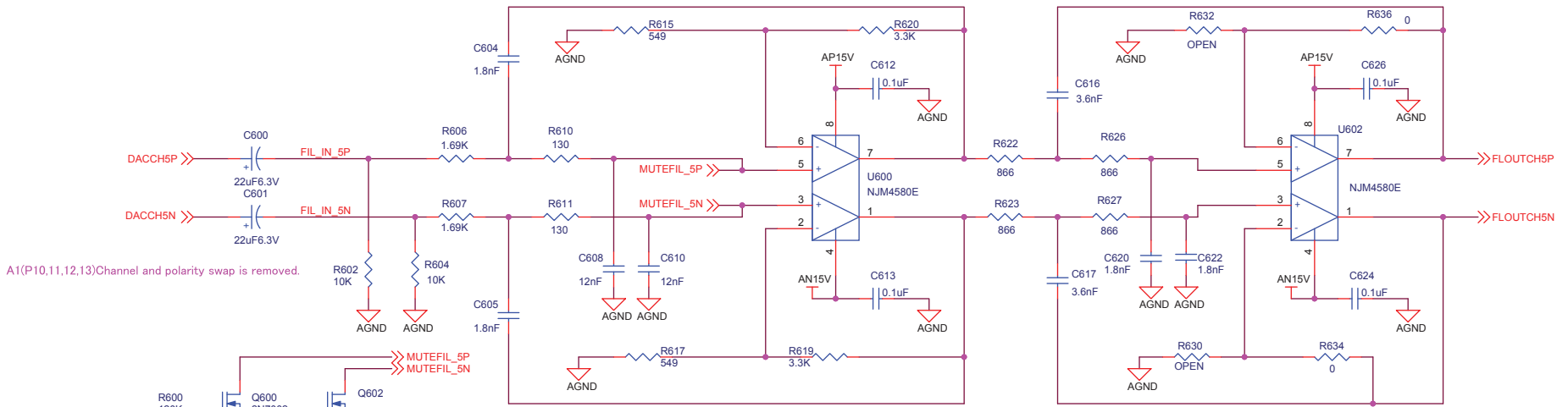


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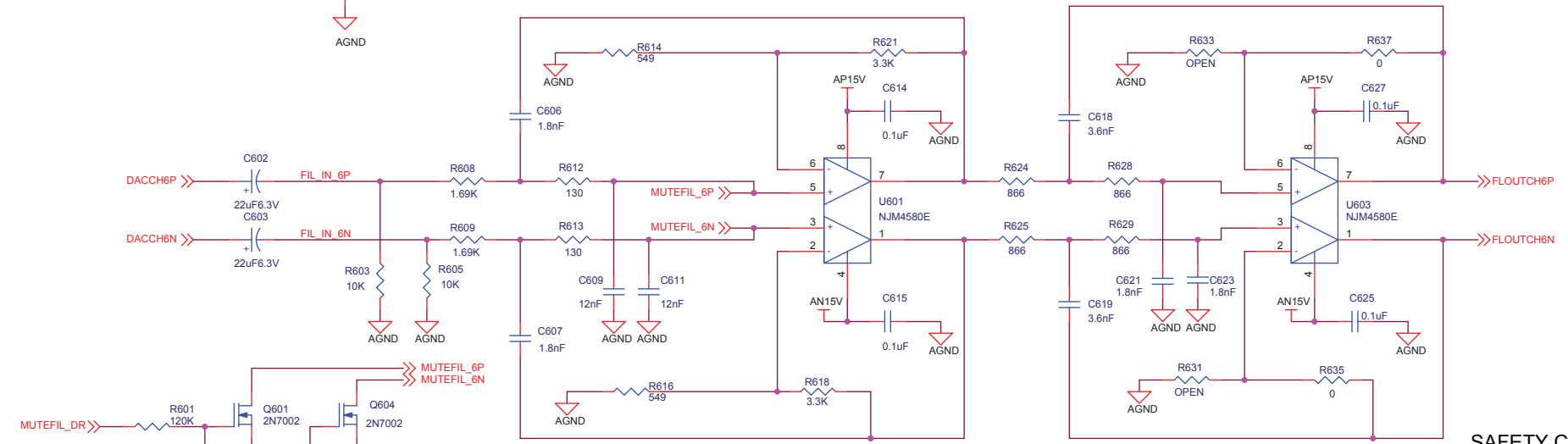


OUTPUT FILTERS CH3-4



A1(P10,11,12,13)Channel and polarity swap is removed.

4TH ORDER BUTTERWORTH SALLEN-KEY MFB HYBRID LOWPASS FILTER
 FIRST STAGE GAIN = 16; SECOND GAIN = 1
 CUTOFF STARTS AT 40KHz, 3dB AT 50KHz

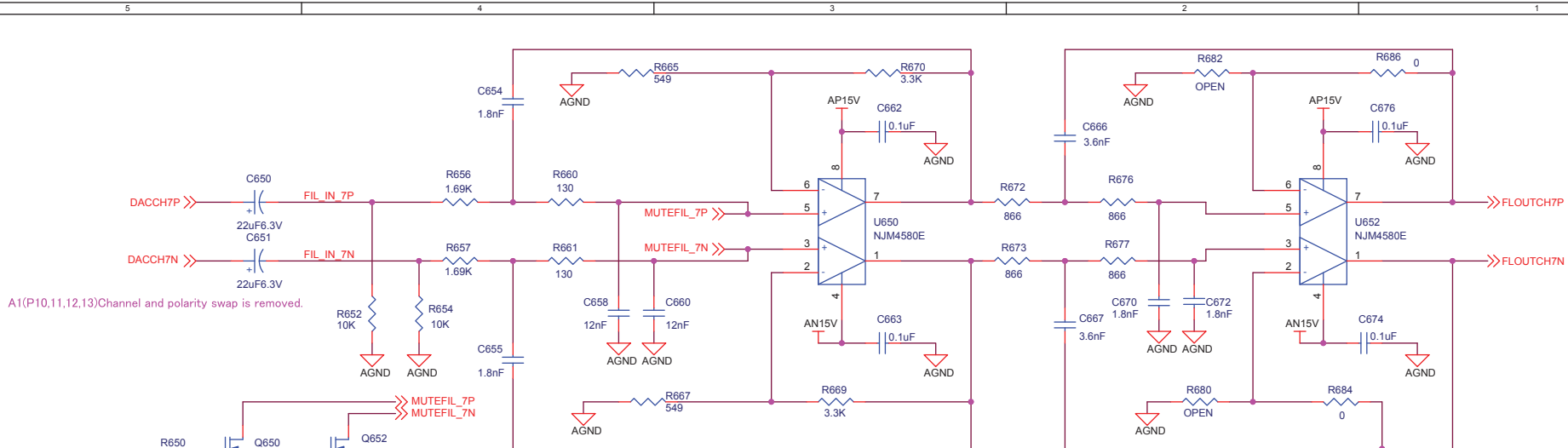


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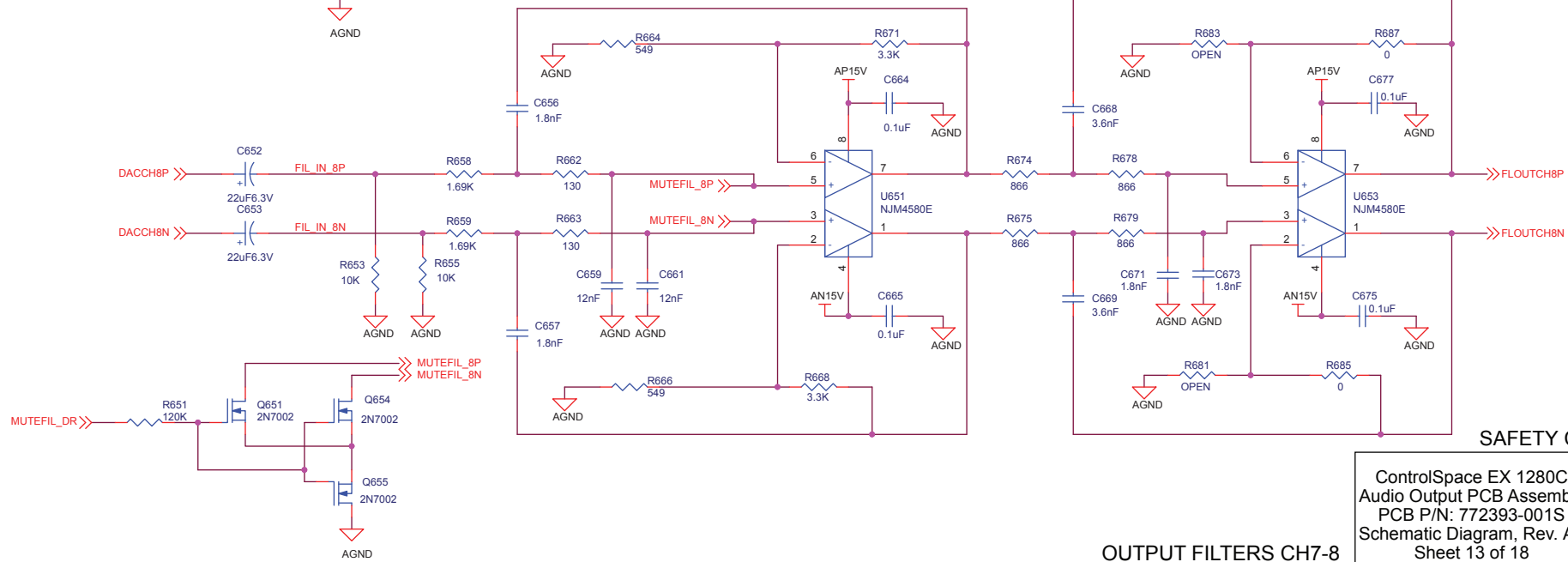
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OUTPUT FILTERS CH5-6



4TH ORDER BUTTERWORTH SALLEN-KEY MFB HYBRID LOWPASS FILTER
 FIRST STAGE GAIN = 16; SECOND GAIN = 1
 CUTOFF STARTS AT 40KHz, 3dB AT 50KHz

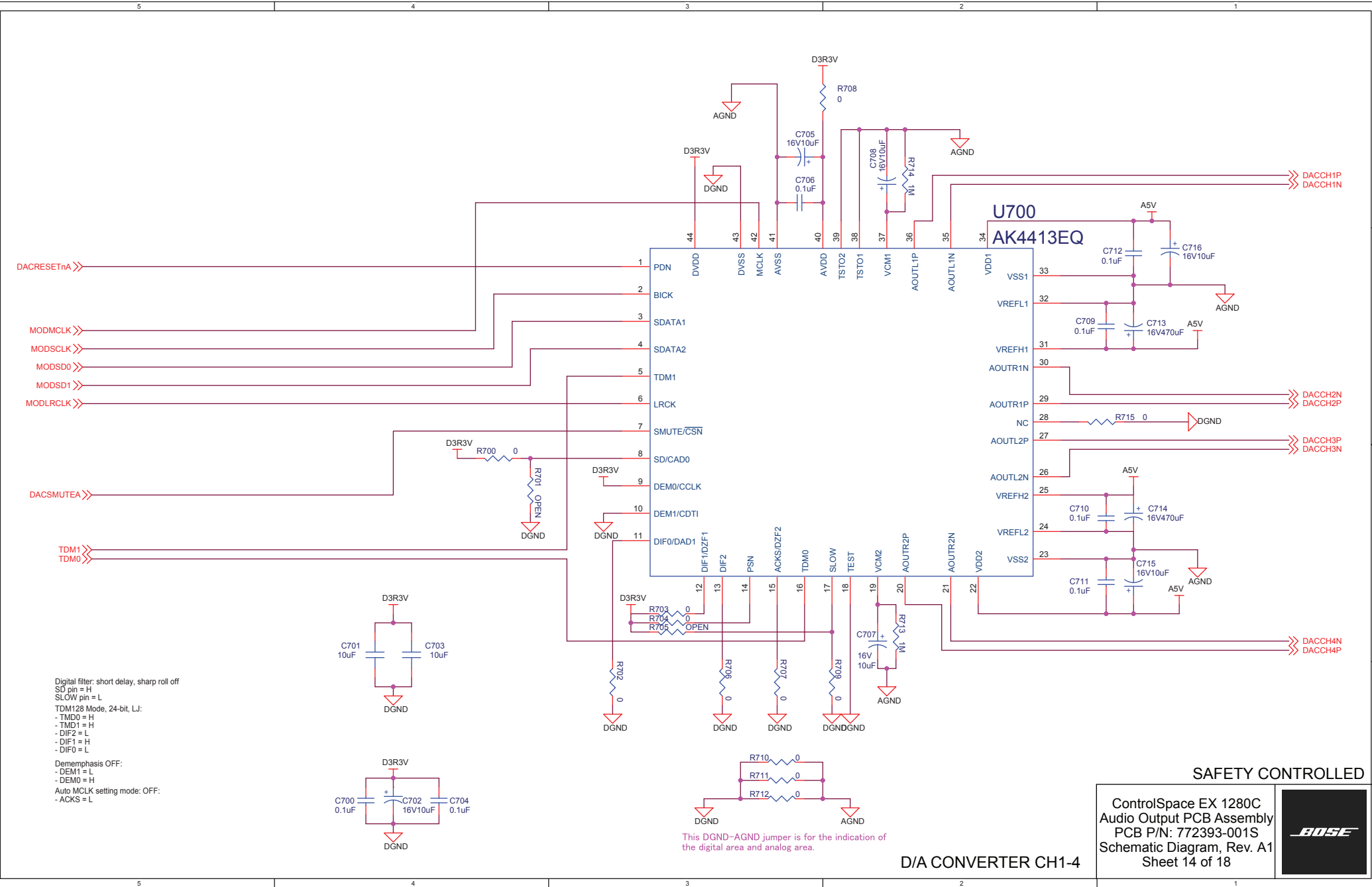


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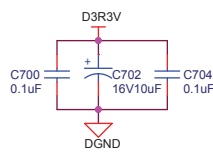
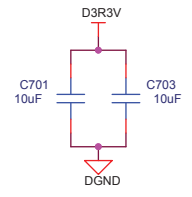
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OUTPUT FILTERS CH7-8



Digital filter: short delay, sharp roll off
 SD pin = H
 SLOW pin = L
 TDM128 Mode, 24-bit, L1:
 - TMD0 = H
 - TMD1 = H
 - DIF2 = L
 - DIF1 = H
 - DIF0 = L
 Dememphsis OFF:
 - DEM1 = L
 - DEM0 = H
 Auto MCLK setting mode: OFF:
 - ACKS = L



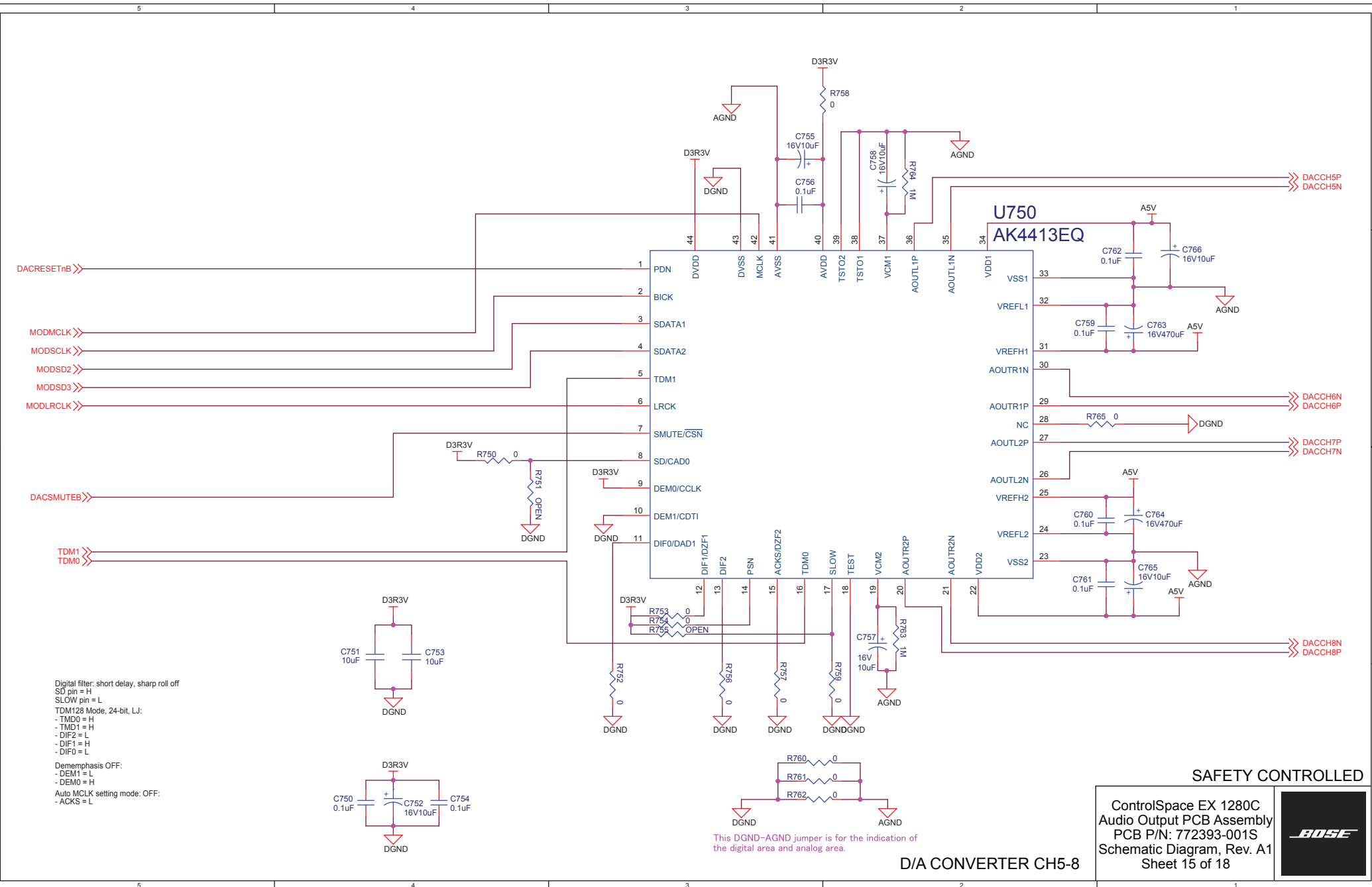
This DGND-AGND jumper is for the indication of the digital area and analog area.

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D/A CONVERTER CH1-4



Digital filter: short delay, sharp roll off
 SD pin = H
 SLOW pin = L
 TDM128 Mode, 24-bit, L1:
 - TMD0 = H
 - TMD1 = H
 - DIF2 = L
 - DIF1 = H
 - DIF0 = L

Demphasis OFF:
 - DEM1 = L
 - DEM0 = H
 Auto MCLK setting mode: OFF:
 - ACKS = L

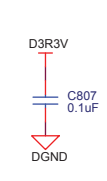
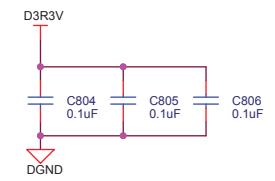
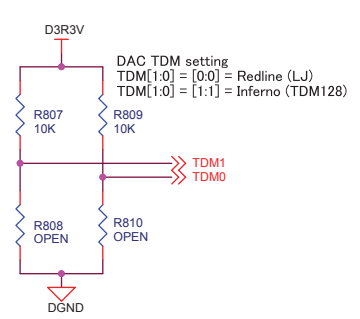
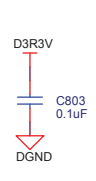
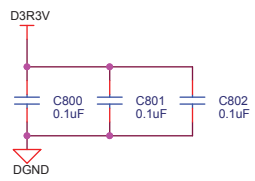
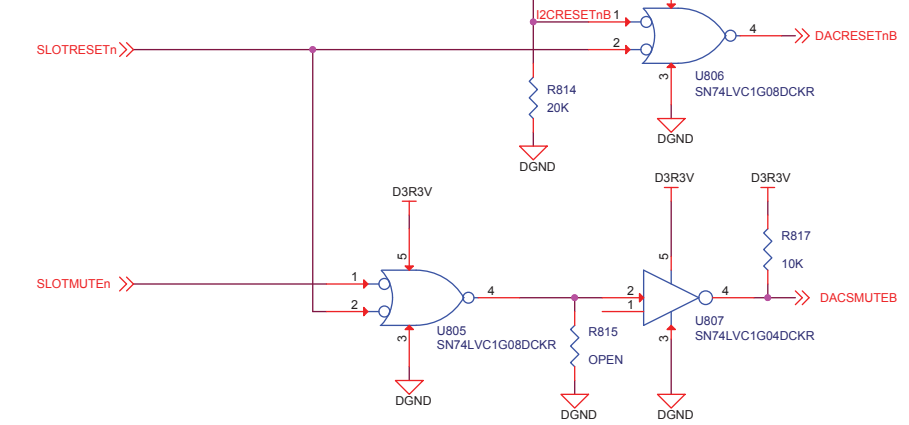
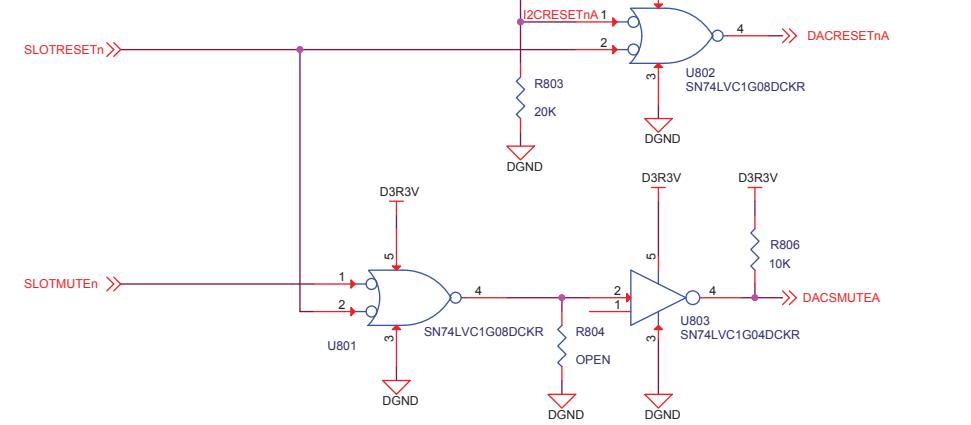
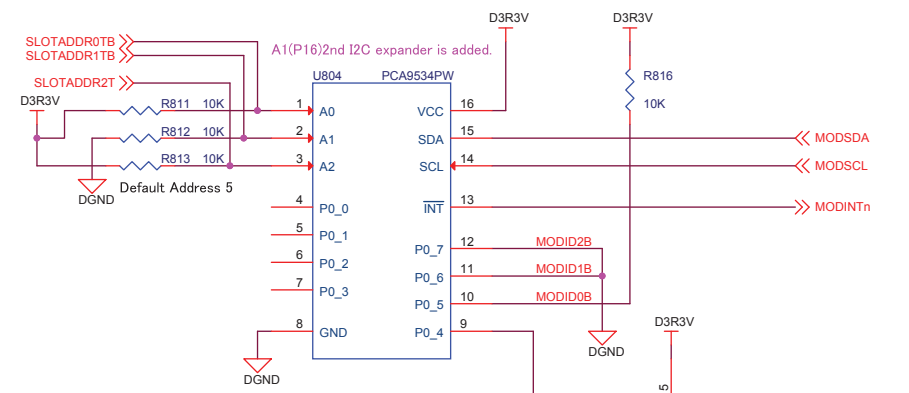
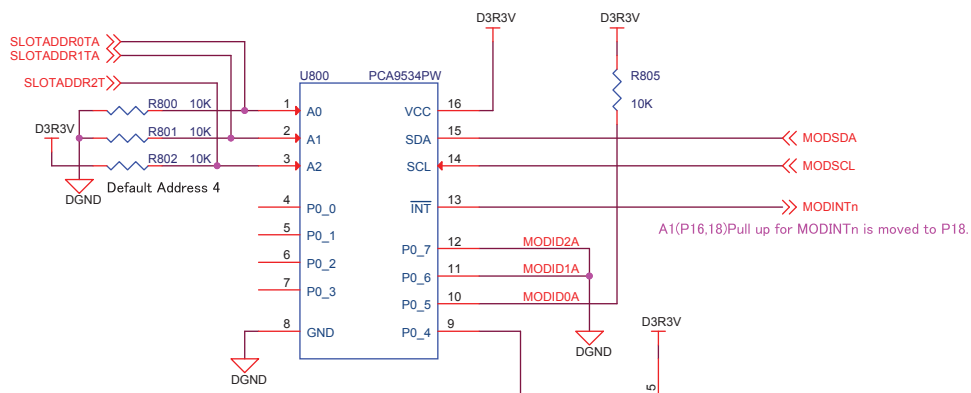
This DGND-AGND jumper is for the indication of the digital area and analog area.

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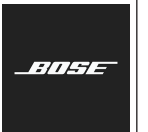


D/A CONVERTER CH5-8



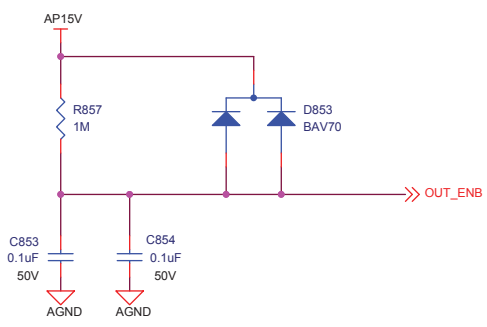
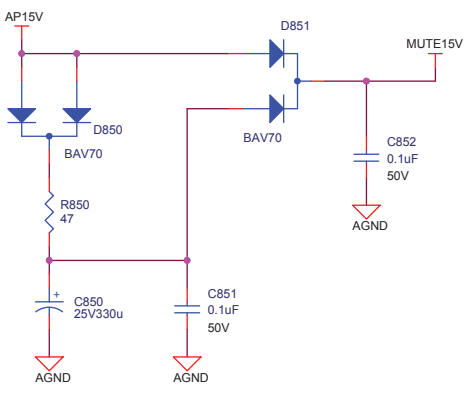
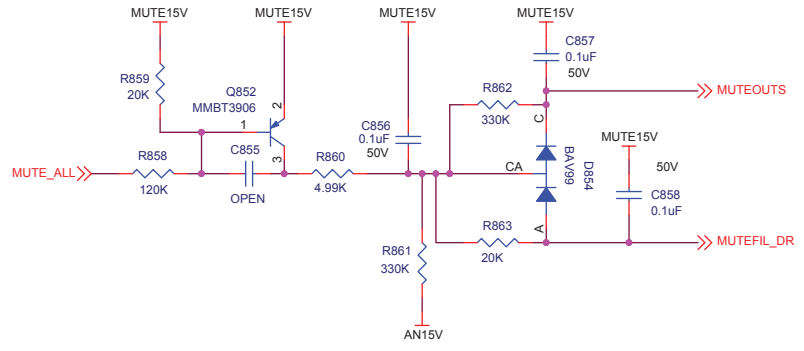
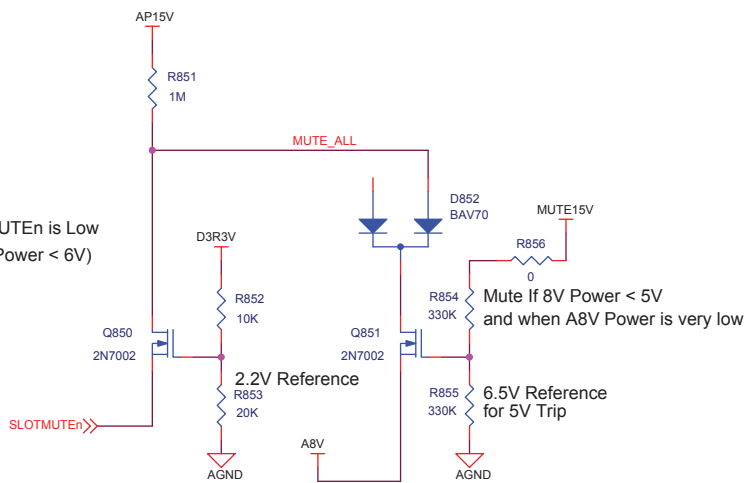
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I2C - MUTE

Mute If SLOTMUTE_n is Low
(Low when A8V Power < 6V)



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POWER OFF MUTE

